



DELHI TECHNOLOGICAL UNIVERSITY

Department of Software Engineering

Labs & Infrastructure

Software Engineering

Officer-in-Charge: Dr Abhilasha Sharma, Associate

Professor

Junior Technical Assistant: Er.Pawan Kumar

Students are exposed to live projects and take requirements from customers directly for implementing IT projects using well known software engineering principles like water-fall model, iterative model, and agile model etc. Students use tools like IBM Rational Rose to capture requirements, different views of systems architecture and design artifacts. They implement well known design patterns like observe-observable pattern, immediate pattern, and factory pattern etc.

Equipments in Lab:

1. Computers and Hardware Peripherals -:

| Lab Name | Manufacturer/Model | Processor/RAM/HDD | Operating System | Unit |
|---------------------------|--------------------|---|--------------------------|------|
| Software Engineering Labs | Accer Veriton | Intel (R) Core (TM) i5-4570 CPU @3.20GHz/ 2.00 GB RAM/500.00 GB HDD | 64-bit Windows Pro 10 | 19 |

2. BenQ project- 01

Wi-Fi: -

Manageable Wi-Fi access point in the lab for access Internet on Desktop and Laptop.

Installed Software & Tools: -

Turbo C++ 4.0 , Code blocks – 13.12 , Dev Cpp - 4.9.9.2, Ms Office.



Software Engineering Lab